

About the College

LORDS Institute of Engineering and Technology (LIET) was established in the year 2003 with a sole objective of providing quality technical education accessible and affordable to youth of our nation. The college has excellent infrastructural facilities and modern laboratories supported by highly competent and dedicated faculty. The college offers Undergraduate programs in engineering branches, namely ECE, EEE, CSE, CSE(AIML), CSE(DS), AIML, IT, ME and CE with a total intake of 1080 students. Four of its undergraduate programs, namely, CSE, ECE, EEE and ME have been accredited by NBA. The college was conferred "Autonomous" status by UGC with effect from AY 2021-22. The college has been accredited by NAAC with 'A' grade. The college also offers postgraduate program in MBA with an intake of 120 students and M.Tech program in CSE, EEE, ME, CE with an intake of 117 students. It has been sanctioned sponsored research projects from UGC, DST, DRDO and JNTUHQ-TEQIP. The college has also been granted funds for EDC, FDPs and MODROBS by AICTE. More than 80% of eligible students are placed in several MNCs. College has entered into MoUs with several reputed organizations for mutual benefits such as students projects, expert lectures, industrial visits, consultancy etc.

About ECE Department

The Department of Electronics and Communication Engineering (ECE) was started in the year 2003 with an initial intake of 120 students. Right from its inception it is continuously striving to impart quality education and promoting competitive spirit among students for academic excellence. The department has well equipped laboratories, good infrastructure, highly qualified and experienced faculty. A good number of research papers are published by the faculty in National/International conference proceedings/Journals. The department has signed MOUs with various organizations to provide real time training to the students. The department was sanctioned FDP under AQIS by AICTE and was successfully executed. The department has received patents in the field of image processing and microwave Engineering.

Aim of the programme

Due to explosive growth in the field of Signal and Image Processing during past three decades, phenomenal advances and attainments are achieved both in the areas of research and applications. The program aims at giving a comprehensive coverage of Signal and Image Processing algorithmic techniques, balancing theory, hands-on, applications and standards. It would render a wide coverage of 1-D and 2-D signal processing, supported by an essential foundation for a full understanding of the signal and image processing concepts. The main objectives of this FDP are:

- To update the participants with new techniques, approaches, algorithms and methods in the thematic area of Signal and

Image Processing with sufficient analytical foundation and framework.

- To provide an in-depth knowhow of supporting hands-on tools for verification/implementation of Signal and Image Processing algorithms, on simulation / hardware platforms.
- To appraise the participants of pedagogical skills on Signal and Image Processing theory and practice.
- To unleash the hidden research potential in the domain of Signal and Image Processing to the aspiring researchers.
- To cover case study of typical applications like medical imaging, remote sensing, biometric authentication, etc.
- To facilitate the participants for an industrial visit to get practical exposure on state of the art signal and image processing techniques.

Indicative Topics:

- Mathematical tools for continuous and discrete-time systems
- Sampling techniques
- Spectral estimation
- IIR and FIR Filters
- Multi-rate signal processing and filter bank theory
- Time-frequency analysis
- Image & Speech enhancement, Segmentation and Compression
- Morphological image processing and Pattern recognition
- Adaptive signal processing
- Signal processing for 5G, Speech and Video
- Image processing for medical images and remote sensing
- Linear algebra, Neural networks, statistical methods for machine learning
- Machine learning in image & speech processing

Expected outcomes:

After completion of the FDP, participants

- Will have in-depth knowledge and hands-on experience in signal and image processing concepts and tools.
- Will develop research and innovative orientation.
- Will be able to apply signal and image processing algorithms in various applications.
- Will be able to develop a logical understanding and comprehensive view of the applications of various signal and image processing techniques.

- Will be able to solve industry problems using practical approaches including conventional to latest applications and will build substantial potential of problem handling.
- Will have an enhanced pedagogical skill.

Resource Persons:

Eminent personalities with expertise in related fields from IITs, NITs, IIITs, HCU, NRSC, ADRIN, BITS (HC), OU and Other reputed institutions will deliver talks on various topics. Speakers from industries are also expected to deliver as part of the course.

Eligibility:

Faculty from AICTE approved engineering colleges and institutions, and R&D organizations with basic degree in Electronics and Communication Engineering, Electrical and Electronics Engineering, Electronics and Instrumentation Engineering, Electronics and Computer Engineering and those who are associated with Signal Processing and Image Processing.

Registration Fee/accommodation/selection criteria

No registration fee for participation. Outstation participants from AICTE approved institutions will be given TA/DA and accommodation as per AICTE/Institute norms. Selection will be done based on **first-cum-first serve basis** and the confirmed candidates will be notified immediately. The maximum number of participants will be **50(Fifty)**.

The list of selected participants will be notified in the institute's website www.lords.ac.in and to their personal e-mail IDs.

How to apply:

Registration for FDP can be done using the Google form link as mentioned below.

<https://docs.google.com/forms/d/1-We4o1NBe9MWoNvhAZB9rGq3uG6Pctrv3wh5B8bg/viewform>

Last date for receipt of registration form: 10.09.2022

Intimation of selection on or before: 12.09.2022

For further details contact:

Dr.C.Venkata Narasimhulu,

Coordinator

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ATAL AICTE Sponsored
Two-Week
Faculty Development Program
(Application Number: 1651989169)

on

**Recent Trends in Signal and Image
Processing with hands-on training using
MATLAB**

19 - 30 September 2022

Registration Form

1. Name: Male/Female:.....
2. Designation:
3. Organization:
4. Teaching Exp: Industry Exp:.....
5. Highest qualification:.....
6. Specialization:.....
7. Areas of interest:.....
8. Address for Communication:
.....
9. Mobile:.....
10. E-mail:.....

Declaration

The information furnished above is true to the best of my knowledge. I agree to abide by the rules and regulations governing the FDP.

Place:

Signature of the Applicant

Sponsorship

Mr./Ms./Dr is an employee of our institution and is hereby sponsored for attending the above FDP.

Place:

Signature of the Principal

Note: Please send the scanned copy of the filled and duly signed registration form to the email: narasimhulucv@gmail.com

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*Organized
by*

**Department
of**

Electronics and Communication Engineering

LORDS

Institute of Engineering and Technology

(Accredited by NAAC with „A” grade & NBA, UGC Autonomous
Institution and affiliated to Osmania University)

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