



# LORDS INSTITUTE OF ENGINEERING AND TECHNOLOGY

Approved by AICTE/Affiliated to Osmania University/Estd.2002  
(UGC AUTONOMOUS)

B.E. III SEMESTER END EXAMINATIONS(Backlogs)(LR21)(A.Y.2024-25): January/February 2025


**Timings: 02:00 PM to 05:00 PM**

## TIME TABLE

BRANCH	DATE & DAY				
	21-01-2025 (Tuesday)	23-01-2025 (Thursday)	25-01-2025 (Saturday)	29-01-2025 (Wednesday)	31-01-2025 (Friday)
<b>CIVIL</b>	MATHEMATICS-III (PDE & PS) (U21MA302)	STRENGTH OF MATERIALS-I (U21CE301)	ENGINEERING GEOLOGY (U21CE302)	SURVEYING AND GEOMATICS (U21CE303)	ELEMENTS OF ELECTRICAL ENGINEERING (U21EE305)
<b>EEE</b>	ANALOG ELECTRONICS (U21EE301)	ELECTRICAL CIRCUIT ANALYSIS (U21EE304)	ELECTRICAL MACHINES-I (U21EE302)	ELECTROMAGNETIC FIELDS (U21EE303)	ENGINEERING MECHANICS (U21ME301)
<b>MECH</b>	MATHEMATICS-III (PDE & PS) (U21MA302)	THERMO DYNAMICS (U21ME303)	MANUFACTURING PROCESSES (U21ME302)	ENGLISH FOR TECHNICAL COMMUNICATION (U21EN301)	ENGINEERING MECHANICS (U21ME301)
<b>ECE</b>	MATHEMATICS-III (PROBABILITY AND STATISTICS) (U21MA301)	ELECTRONICS DEVICES (U21EC301)	SIGNALS AND SYSTEMS (U21EC302)	ENGLISH FOR TECHNICAL COMMUNICATION (U21EN301)	NETWORK THEORY (U21EC303)
<b>CSE</b>	DISCRETE MATHEMATICS (U21CS301)	PYTHON PROGRAMMING (U21CM301)	DATA STRUCTURES (U21CS302)	OPERATIONS RESEARCH (U21ME307)	DIGITAL ELECTRONICS AND COMPUTER ORGANIZATION (U21EC304)
<b>IT</b>	MATHEMATICS-III (PROBABILITY AND STATISTICS) (U21MA301)	PYTHON PROGRAMMING (U21CM301)	DATA STRUCTURES (U21CS302)	ENGLISH FOR TECHNICAL COMMUNICATION (U21EN301)	DIGITAL LOGIC DESIGN (U21EC305)
<b>CSM</b>	MATHEMATICS-III (PROBABILITY AND STATISTICS) (U21MA301)	PYTHON PROGRAMMING (U21CM301)	DATA STRUCTURES (U21CS302)	ENGLISH FOR TECHNICAL COMMUNICATION (U21EN301)	DIGITAL ELECTRONICS AND COMPUTER ORGANIZATION (U21EC304)
<b>CSD</b>	OPERATING SYSTEMS (U21CD301)	PYTHON PROGRAMMING (U21CM301)	DATA STRUCTURES (U21CS302)	DATA BASE MANAGEMENT SYSTEM (U21CD302)	DIGITAL ELECTRONICS AND COMPUTER ORGANIZATION (U21EC304)
<b>AIML</b>	MATHEMATICS-III (PROBABILITY AND STATISTICS) (U21MA301)	PYTHON PROGRAMMING (U21CM301)	DATA STRUCTURES (U21CS302)	ENGLISH FOR TECHNICAL COMMUNICATION (U21EN301)	DIGITAL ELECTRONICS AND COMPUTER ORGANIZATION (U21EC304)

Note:

- This timetable is applicable to the students admitted in academic year 2021-2022 & 2022-2023
- Hod's and exam branch coordinators are requested to go through the time table carefully, if any issues found regarding subjects or other issues should be intimated on or before 18-01-2025 to the exam branch.

  
 Controller of Examinations  
 Lords Institute of Engineering and  
 Sy. No. 32, Himayathsagar,  
 Hyderabad-500091.

Copy to:

1. The Dean 1<sup>st</sup> Year, Dean Academics, Dean IQAC, The HoD'S of H&S/CIVIL/EEE/MECH/ECE/CSE/IT/CSM/CSD, with a request to bring it to the notice of all concerned.
2. The Librarian, A.O Office & Admission Office for kind information.
3. Circulation Among the all-2nd Year&3<sup>rd</sup> Year B.E faculties and students.
4. Notice board.

  
 Principal

**PRINCIPAL**  
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